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Additional Programs for the Electronics Module Part No. 152063

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1.1 Additional Programs for the Electronics Module

For fans of digital technology, these additional functions are provided in the fischertechnik Electronics Module (part no. 152063) and can be selected using the DIP switches. The added features are not described in the booklets, as they are designed as additional projects after the construction of the models contained in the PROFI Electronics kit (part no. 524326).

As with the programs described in the booklet, the setting of the DIP switches only becomes effective after a short disruption of the supply voltage (i.e. switch it off and on again).

The special functions include; AND logic gate, OR logic gate, XOR logic gate, D flip-flop with reset, JK (RS) flip-flop, monostable, threshold and differential switch (analog comparator), binary or BCD counter, dual programmable frequency divider, as well as lighting effects and frequency generator.

The following is a description of these additional functions.

1



1.2 Wiring of the inputs and outputs

For additional programs, with up to 4 outputs and up to six inputs, the I/O numbering is from left to right as shown below:

Outputs				
N	1	M2		
01	O2	O3	O4	



Inputs						
ľ	1	Ľ	2	13		
E1	E2	E3	E4	E5	E6	

Examples: O2 = right socket of M1 O3 = left socket of M2 E1 = left socket of I1E6 = right socket of I3

The inputs with the prefix "I" correspond to the normal inputs, they can (depending on the selected program) handle both analog and digital signals. In an unconnected or "open" condition, they are inactive (= 0).

The inputs with the prefix "E" are the additional inputs. The odd numbered "E" inputs have a pull-up resistor, these inputs are normally in the "1" state and can be changed to the "0" state by connecting to "-" (ground or 0V). The even numbered "E" inputs have a pull-down resistor, these inputs are normally in the "0" state and can be changed to the "1" state by connecting to "+" (positive or 9V supply).

You can connect any output (O1 to O4) of an Electronics module to any "E" input directly. As the Electronics module's output always has a defined output level (0V or 9V), this satisfies the input requirement without the need for additional components or connections.



When connecting pushbuttons to "E" inputs, it is recommended that the following circuit is used:



By using this method of wiring, a "-" is connected to the "E" input when the button is not pressed and a "+" is connected to the "E" input when the button is pressed.

Other electronic devices (e.g. phototransistors) can be connected, but please note the following:

- For E2, E4, E6 inputs: connect the red connection of the phototransistor to "+" and the other terminal of the phototransistor at the respective "E" input.
- For E1, E3, E5 inputs: connect the red connection of the phototransistor to the "E" input and the other terminal of the phototransistor to "-".
- NPN transistors may be directly connected between E1, E3, E5 and "-". To connect an NPN transistor between "-" and E2, E4 or E6, a pull-up resistance¹ is required for the correct operation.

Due to debouncing and the scanning interval of the electronic module, a 15ms signal delay may occur. Note the inputs are scanned at 100Hz (100 times per second).

¹ In the models merry-go-round and sliding door with contact switch an NPN transistor input is connected to the Electronics Module. Instead of the LED used in the "touch switch" circuit in the booklet, a second resistor (a pull-up resistor) is used. The transistors T1 and T2 are connected in, what is called, a "Darlington pair" configuration. When the contact switch is "open", both transistors T1 and T2 are "off" and 9V is connected to the input via the "pull-up resistor". A "1" state will then appear at the input. When the switch is touched a small amount of current flows to the base of T1 which is amplified by the "Darlington pair", turning T2 on and connecting 0V to the input. A "0" state then appears at the input.

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1.3 Additional programs for digital technology

1.3.1 AND / OR logic gate



Truth Ta	Truth Table - AND Logic Gate					able - OR	Logic Ga	ite	
E1	E2	E3	01	02	E1	E2	E3	01	02
E4	E5	E6	O3	O4	E4	E5	E6	O3	O4
0	0	0	0	1	0	0	0	0	1
1	0	0	0	1	1	0	0	1	0
0	1	0	0	1	0	1	0	1	0
1	1	0	0	1	1	1	0	1	0
0	0	1	0	1	0	0	1	1	0
1	0	1	0	1	1	0	1	1	0
0	1	1	0	1	0	1	1	1	0
1	1	1	1	0	1	1	1	1	0
Legend	$\cdot 0 = 0V = 1$	- 9V							

Legend: 0 = 0V, 1 = 9V



1.3.2 XOR Logic Gate

DIP - Switch Position	4 5
AND / OR Logic Gates	XOR Logic Gate
Pot left (counterclockwise)	Pot right (clockwise)
E1 0 (01)	E1 0 (01)
$E2 \longrightarrow \& \qquad \overline{\Omega}(\Omega^2)$	E2 $= 1$ $\overline{\Omega}(\Omega^2)$
E3	E3
E4 (O3)	E4 () (O3)
E5 ≥ 1 \overline{Q} (O4)	E5 \longrightarrow = 1 \overline{Q} (O4)
E6	E6

Truth Table - AND Logic Gate					Truth Ta	able - XO	R Logic C	Gate	
E1	E0	Eo	01	$\cap 2$	E1	E2	E3	01	02
		ES	01	02	E4	E5	E6	O3	O4
0	0	0	0	1	0	0	0	0	1
1	0	0	0	1	1	0	0	1	0
0	1	0	0	1	0	1	0	1	0
1	1	0	0	1	1	1	0	0	1
0	0	1	0	1	0	0	1	1	0
1	0	1	0	1	1	0	1	0	1
0	1	1	0	1	0	1	1	0	1
1	1	1	1	0	1	1	1	1	0
Truth Ta	able - OR	Logic Ga	ate						
E4	E5	E6	O3	O4					
0	0	0	0	1					
1	0	0	1	0					
0	1	0	1	0					
1	1	0	1	0					
0	0	1	1	0					
1	0	1	1	0					
0	1	1	1	0					

 1
 1
 1
 0

 Legend: 0 = 0V, 1 = 9V
 1
 0
 1
 0



1.3.3 D Flip-flop with Reset, JK-(RS) Flip-flop and NOT Logic Gate (inverter)

DIP - Sv	vitch Pos	ition									
D Flip-flop with Reset					JK (R	S) Flip	-flop a	nd NO	T Logi	c Gat	e
Pot left (countercle	ockwise)		\bigcirc	Pot ri	ght (clo	ckwise))	
E1 - E2 - E3 - E4 - E5 - E6 - E1 / E2 E2 / E5	D C R D C R = D = CLOCK		2 (O1) 2̄ (O2) 2̄ (O3) 2̄ (O4)		E1 E2 E3 E4 E5 E6 E1 = 3 E2 = 5		S J C K R		Q (O1) Q (O2) Q (O3) Q (O4)		
E3 / E6	= RESET				E3 = E4 = E5 =	K RESET	-				
Truth Ta	able - D F	lip-flop			Truth	Table	- JK (F	RS) Flir	o-flop		
E1	E2	E3	01	O2		50	511 (.				00
E4	E5	E6	O3	O4	EI	E2	E3	E4	ES	01	02
0 1 X	↑ ↑ ×	0 0 1	0 1 0	1 0 1	0 1 0 1 0 0	0 X X 1 0 1	$\leftarrow \times \times \times \leftarrow \leftarrow$	0 X X 0 1	0 1 1 0 0	- 1 - 1 0 O2 ¹	- 0 1 - 0 1 01 ¹
Legend: to 9V, X	0 = 0V, 1 = Don't C	' = 9V, ↑ Care	= Transiti	on 0V	Lege to 9V Char ¹ E2 a chan	end: 0 = /, X = L nge and E4 ges fro Table -	= 0V, 1 Don't Co = 1:tog m 0 to - NOT 0	= 9V, ↑ are, - = gles 0 <u>1 and 1</u>	> = Tra No Sta 1 and (vise-a-	nsitioi ate D2, versa	n 0V
					Tuur		E6			3	O4
							0		1	-	0

Legend: 0 = 0*V*, 1 = 9*V*



1.3.4 Test circuit - OR / AND / XOR / NOT logic gate D flip-flop / JK (RS) flip-flop





1.3.5 Monostable or One Shot

DIP - Switch	Position			
Set Running	Time Potentiometer adjustable			
I1 (E1/E2) =	retriggerable input, that is, the running time is restarted at each pulse (edge- triggered i.e. responding to a $0V \rightarrow 9V$ transition)			
I2 (E3/E4) =	not retriggerable, that is, the running time must elapse before a new time can start (edge triggered, i.e. responding to a $0V \rightarrow 9V$ transition)			
E5 =	Logic input for time duration (see table below)			
Pot =	Fine adjustment of time duration			
O1 = O2 =	9V output for running time duration O1 inverted			
03/04 =	flashes alternately during the time interval $(O1 = 9V)$			
I1 and I2 can also be triggered via phototransistors. For connection see 1.4.1 Threshold and differential switch (analog comparator)				

Function Table - Monostable Running Time				
E5	E6	Time		
0	0	0.1 – 1 Seconds		
1	0	1 – 10 Seconds		
0	1	10 – 100 Seconds		
1	1	1 – 10 Minutes		

Legend: 0 = 0V, 1 = 9V

1.3.6 Test Circuit - Monostable





1.4 Other Functions

1.4.1 Threshold and differential switch (analog comparator)

DIP - Switch Position	3 4 5
Threshold Switch	Differential Switch
I1 = Phototransistor 1 Pot = Set point adjustment	I2 = Phototransistor 2 I3 = Phototransistor 3 Pot = Hysteresis adjustment (Sensitivity)
O1 = 9V, when E2 exceeds the set point set by the potentiometerO2 = O1 inverted	O3 = 9V, when I2 is greater than I3 O4 = O3 inverted
Application: adjust of light barrier	Application: Orientation of solar cells

1.4.2 Test Circuit - Threshold and differential switch



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1.4.3 Binary or BCD Counter

DIP - S	Switch Position
E1 =	Clock input. Each 0V \rightarrow 9V transition adds one count
E2 =	Count Direction:
	9V = Count Up
	0V = Count Down
E3 =	Counter Enable. At 9V the counter operates
E4 =	Reset input. At 9V, the counter is set to 0 (independent of all other inputs)
E5 =	Count Range:
	9V = 0 to 15 (binary)
	0V = 0 to 9 (BCD – Binary Coded Decimal)
01 =	Counter output, Bit $0 = O1$
O2 =	Counter output, Bit 1 = O2
O3 =	Counter output, Bit 2 = O3
O4 =	Counter output, Bit 3 = O4

1.4.4 Test Circuit – Binary or BCD Counter



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1.4.5 Dual programmable frequency divider

DIP - Switch	Position
E1 = E 2/E3 = E5 = E4/E6 = Pot left = Pot right =	Input Channel 1 Divisor Selection Channel 1 Input Channel 2 Divisor Selection Channel 2 (counterclockwise) debouncing on (clockwise) debouncing off, frequencies can be processed up to 500 Hz. Example: Encoder motor, connect the pulse output (black line) to the input of each channel.
O1 = O2 = O3 = O4 =	Output Channel 1 O1 / 2 Output Channel 2 O3 / 2

Table – Frequency Divisor						
Channel 1		Frequency Divisor				
E2	E3					
Channel 2						
E4	E6					
0	0	16				
1	0	12				
0	1	5				
1	1	3				

Legend: 0 = 0V, 1 = 9V

1.4.6 Test Circuit - Dual programmable frequency divider





1.4.7 Lighting Effects / Frequency Generator

DIP - Switch	Position 1 2 3 4 5			
E1 / E2 / E3 = Selection of type of light effect. These inputs are checked				
Pot =	continuously. i.e. the switches may be changed at any time. speed of light effect or frequency 0–50Hz (O1)			
O1 =	LED 1			
O2 =	LED 2			
O3 =	LED 3			
O4 =	LED 4			
The lighting effects can be changed at any time without restarting the Electronics Module.				

Table - Lighting Effects / Frequency Generator				
E1	E2	E3	E3 Lighting Effect	
0	0	0	4 independent Flashing Lights	
1	0	0	2 Independent Alternate Flashing Lights	
0	1	0	4 Blue-Light Flasher	
1	1	0	Light Chaser	
0	0	1	Construction Site Chaser	
1	0	1	Flickering Fire	
0	1	1	Navigation Lights	
1	1	1	0–50Hz Frequency Output (O1 only)	

Legend: 0 = 0V, 1 = 9V

1.4.8 Test Circuit - Lighting Effects / Frequency Generator





1.5 Transistor Test

DIP – Swit	ch Position	1 2 3 4 5	
E2 = Inp S1 = Sta	ut to Transistor rt Test		
The transistor is OK, if the LED on the Electronic module after pressing S1 shows a short flash.			

1.5.1 Test Circuit – Transistor Test

